



1 / 30

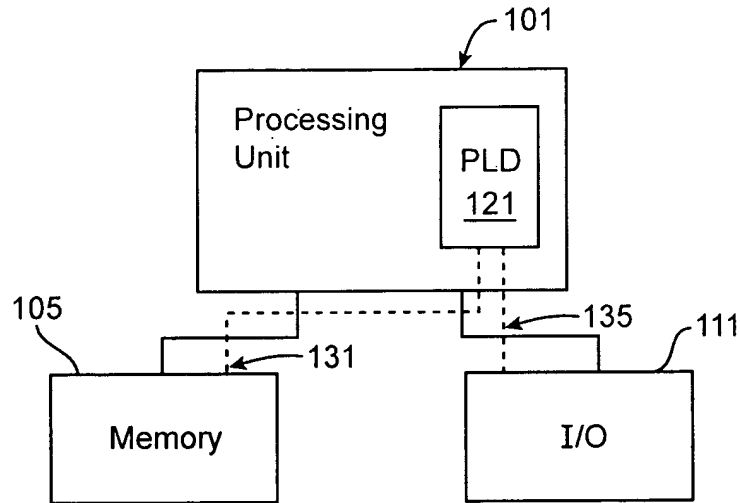


FIG. 1

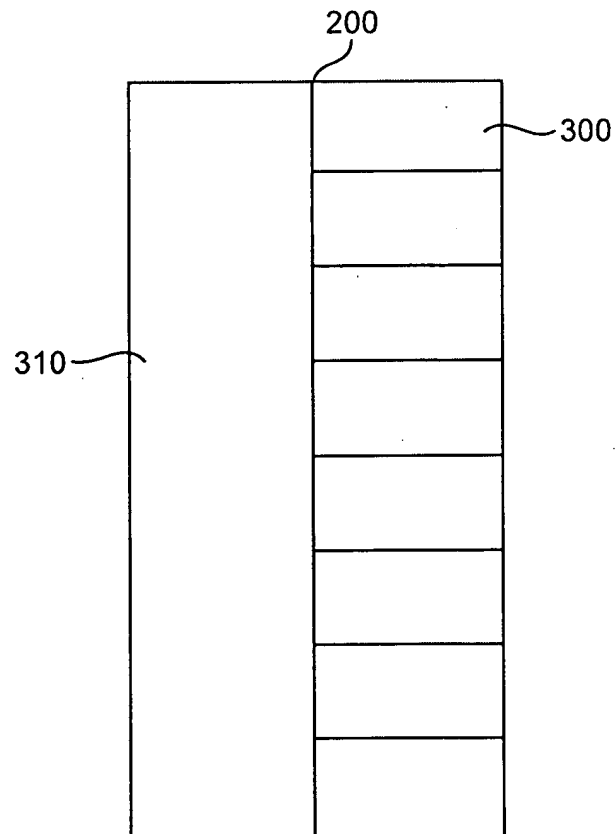
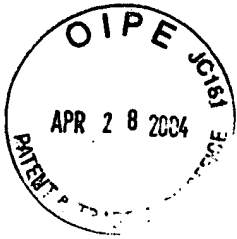


FIG. 3



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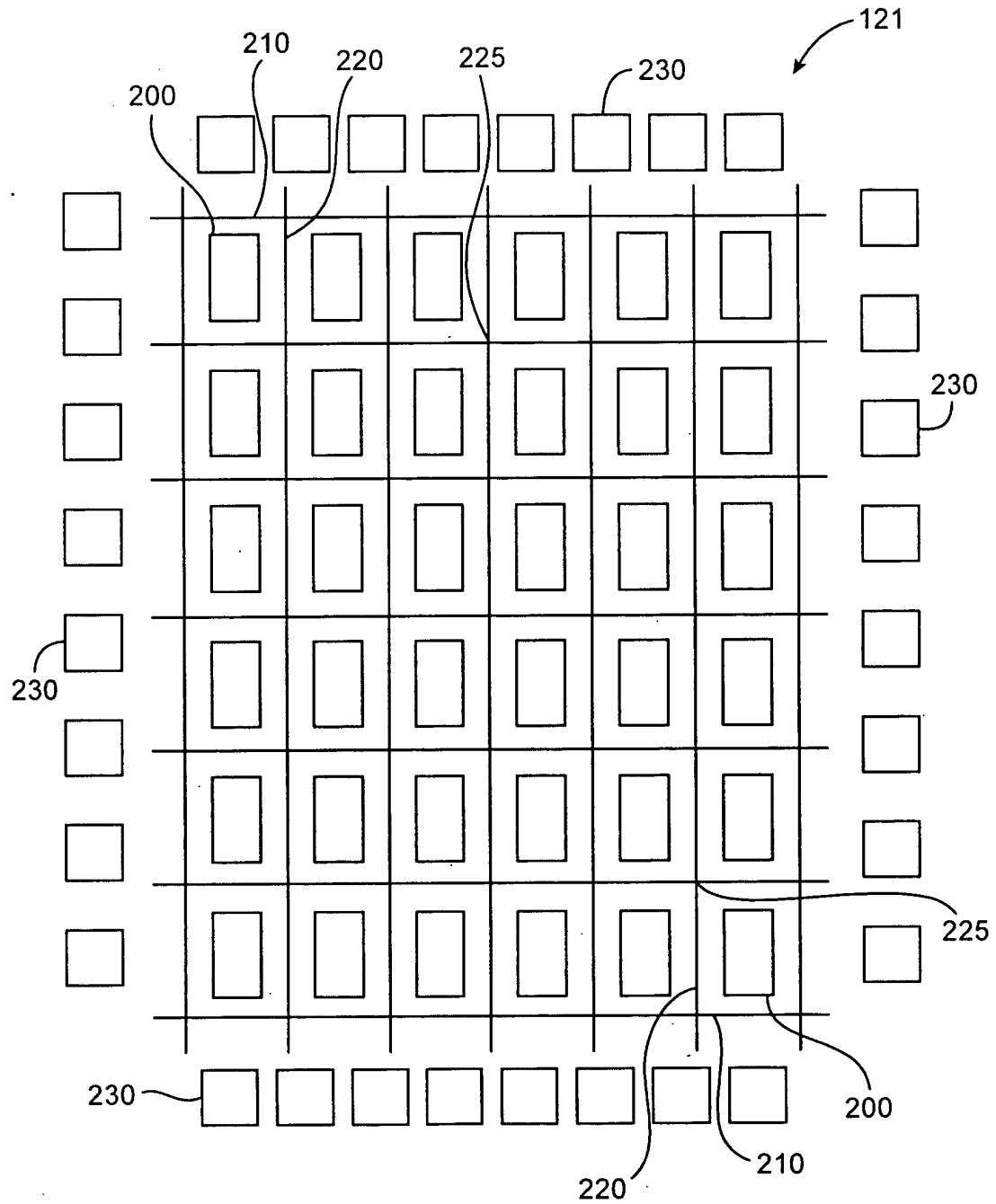


FIG. 2





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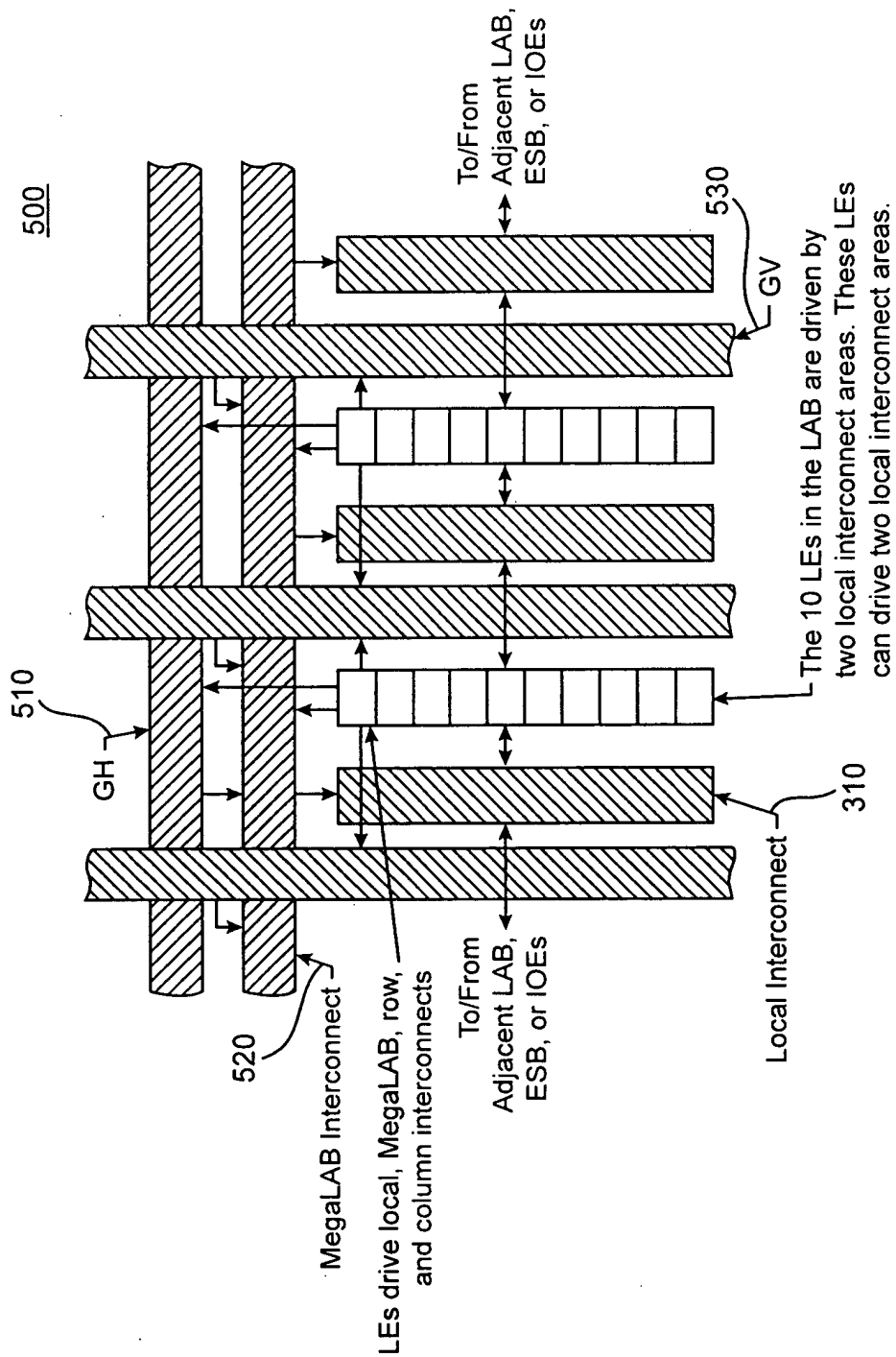


FIG. 5



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600

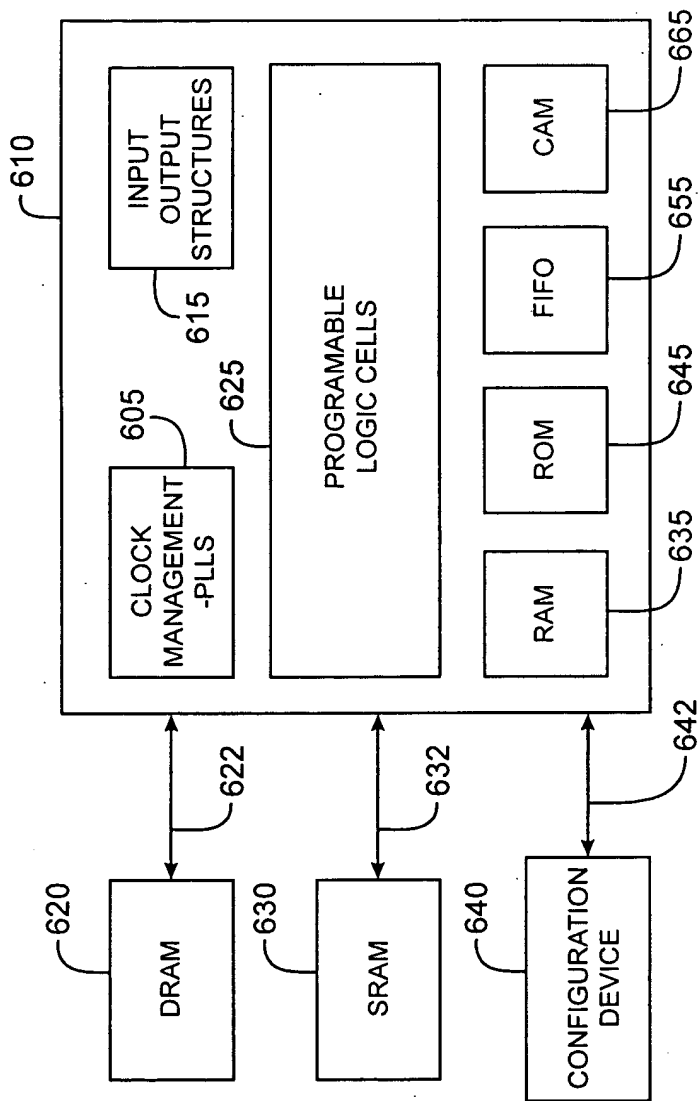


FIG. 6



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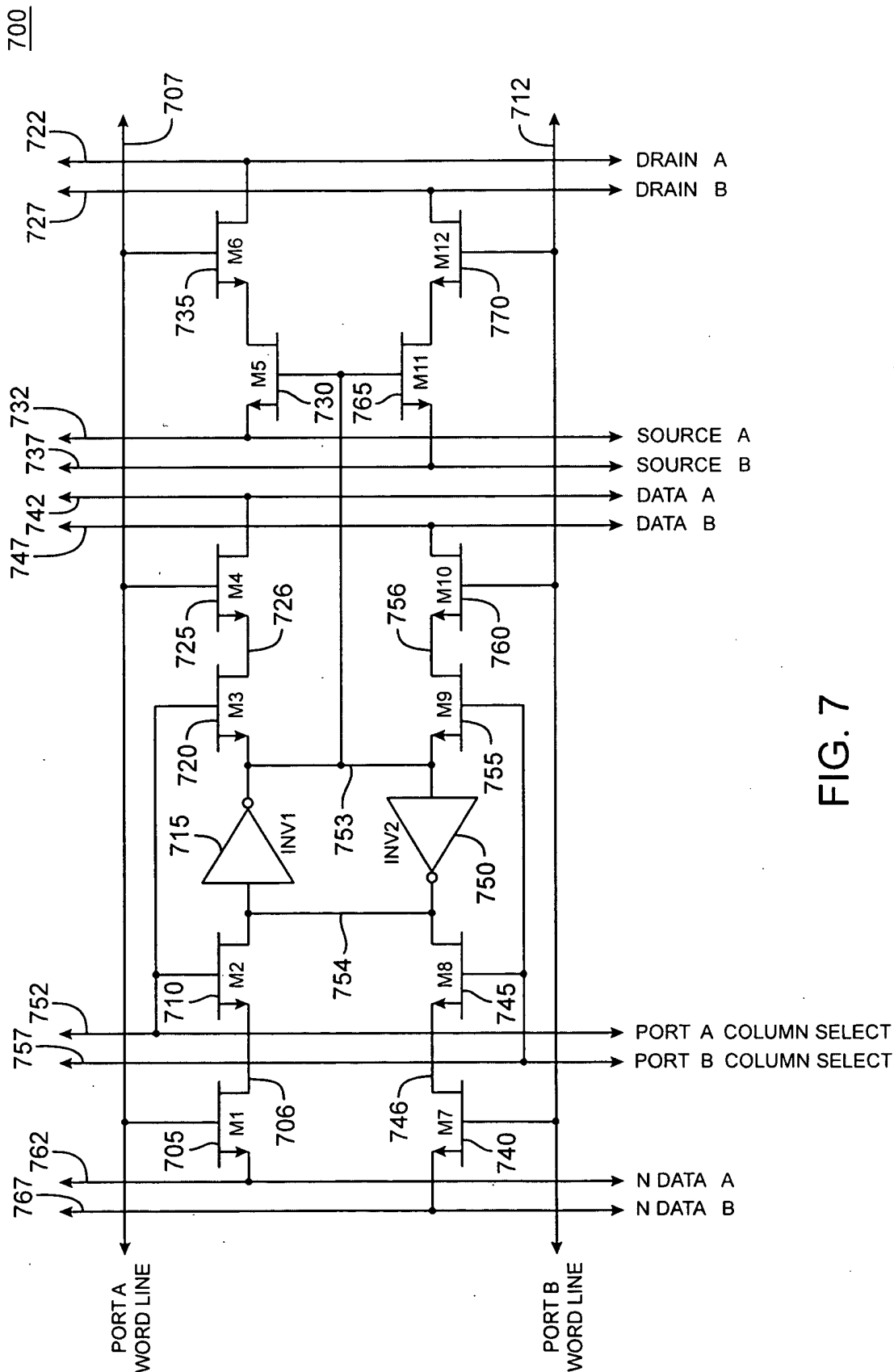


FIG. 7



FIG. 8



900

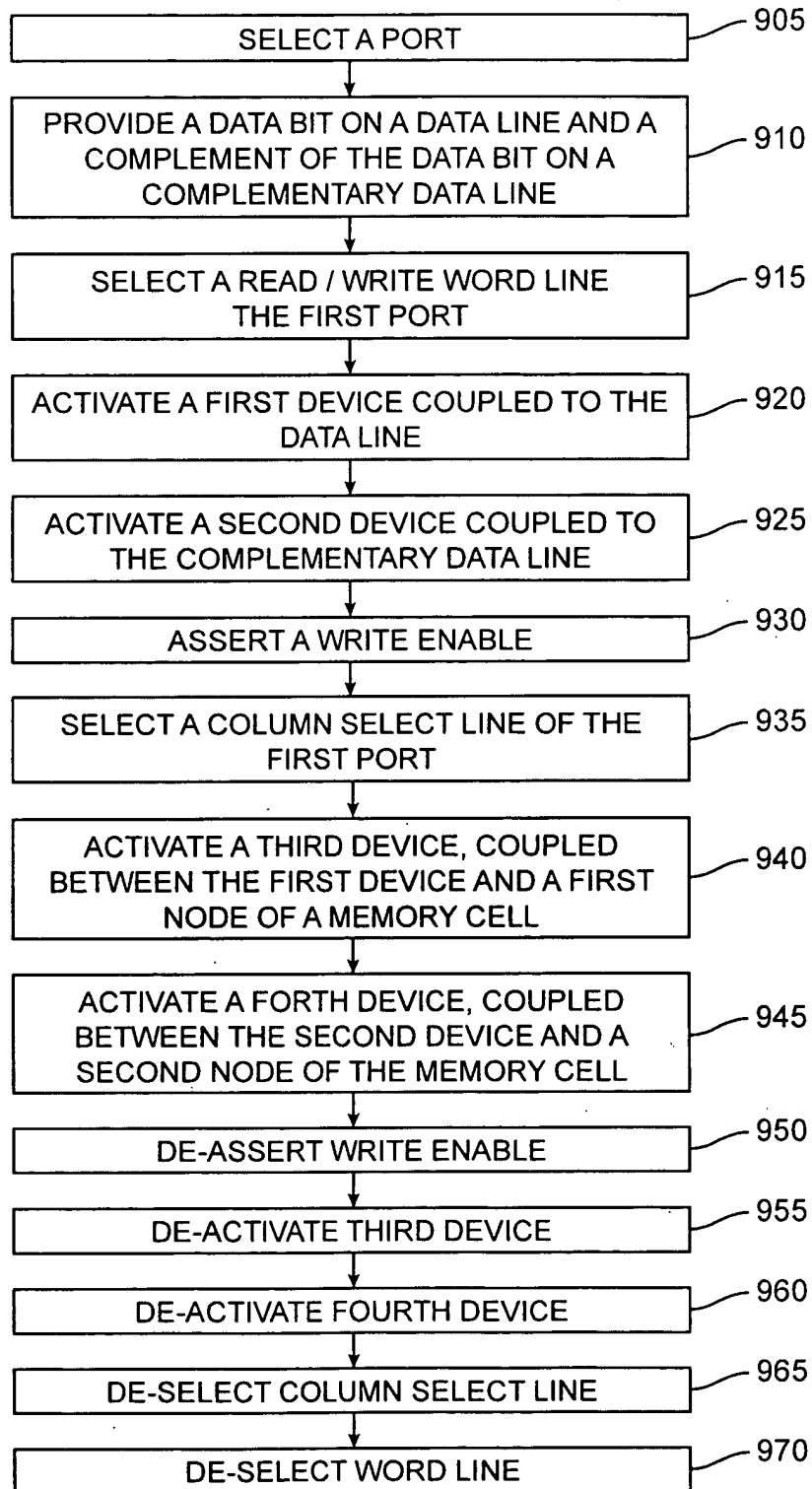


FIG. 9



1000

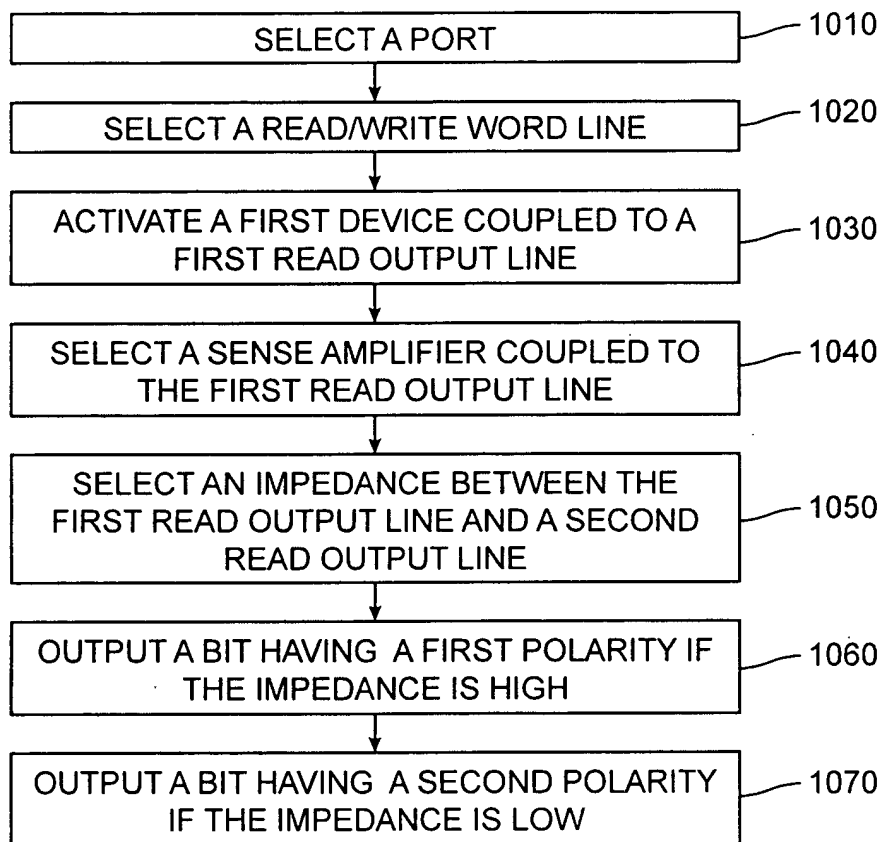


FIG. 10



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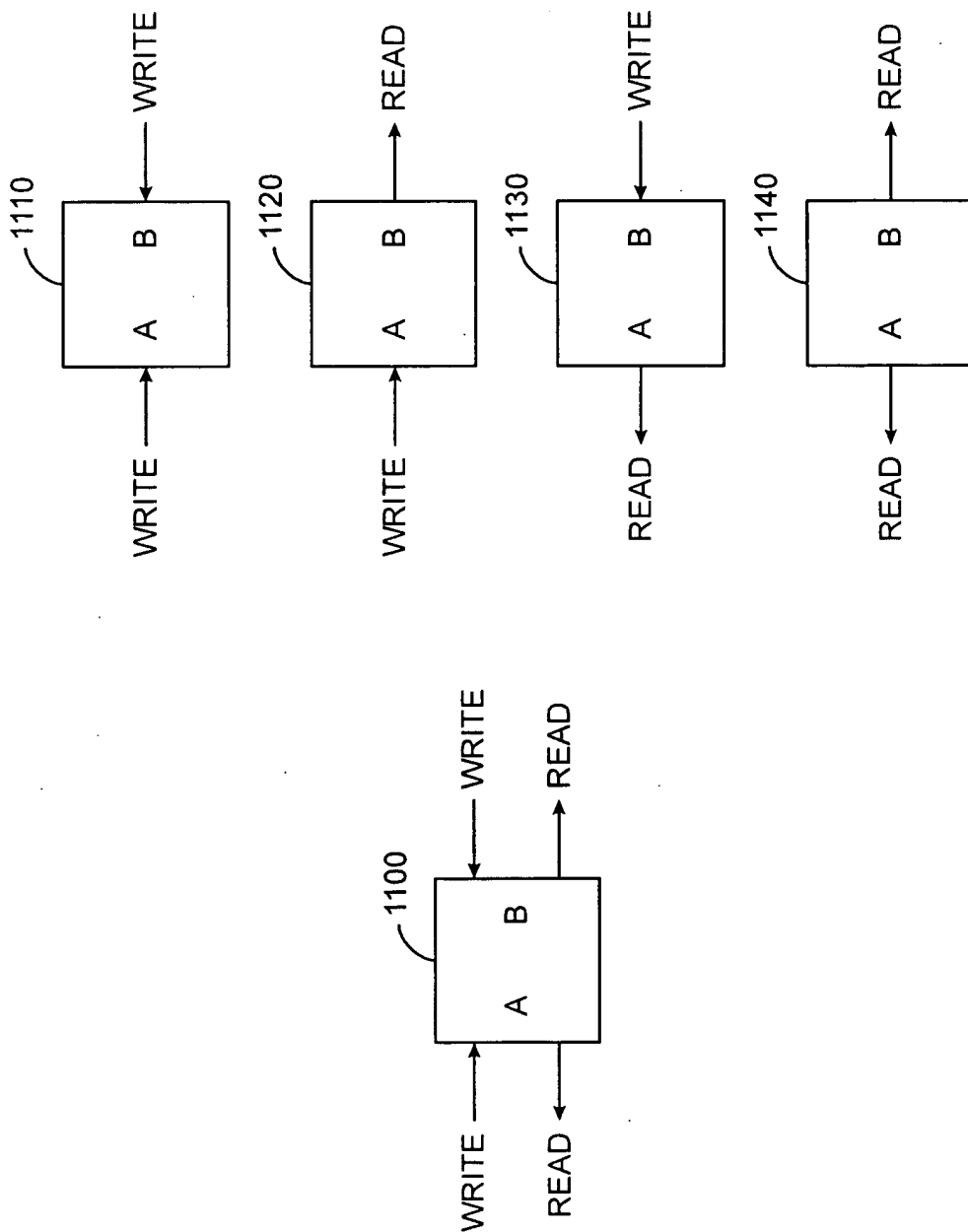


FIG. 11



1200

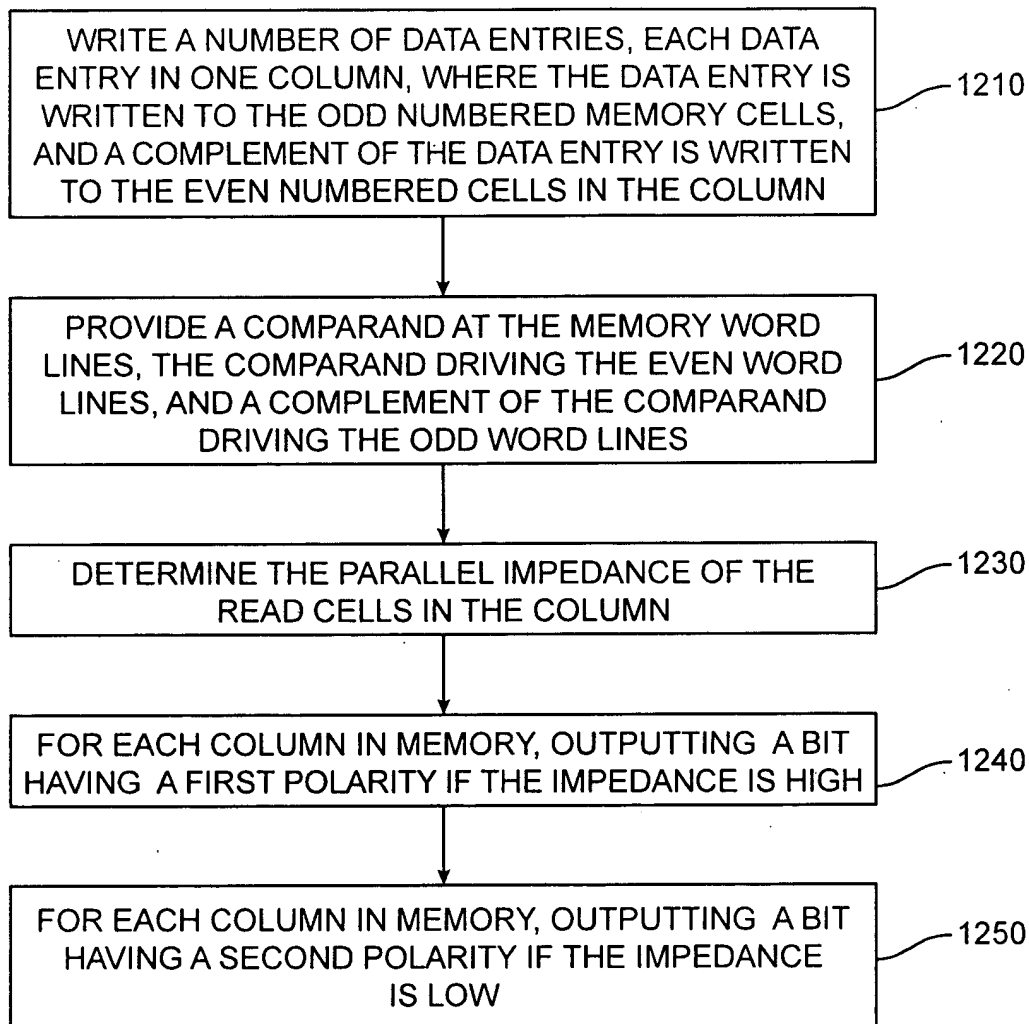


FIG. 12

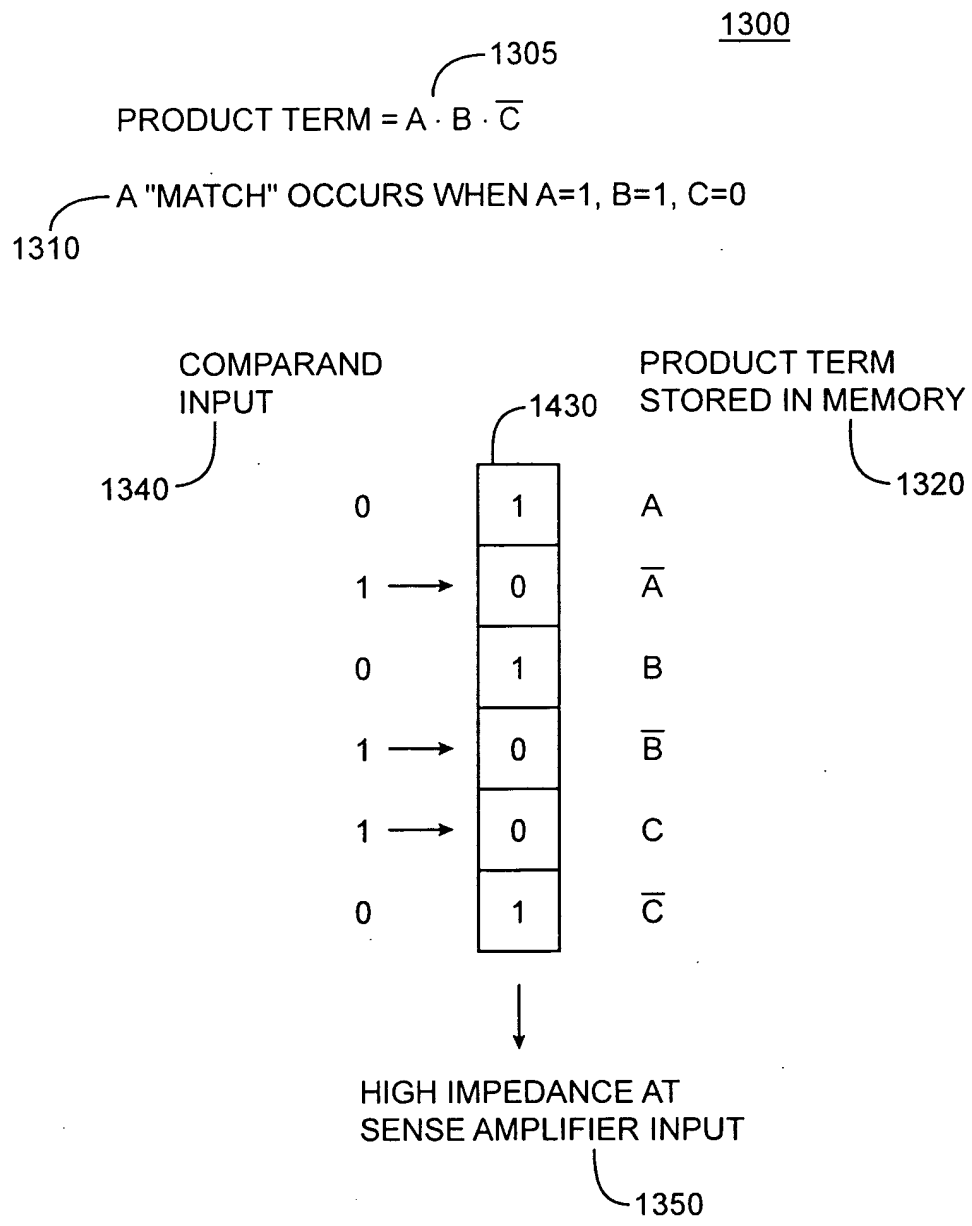


FIG. 13



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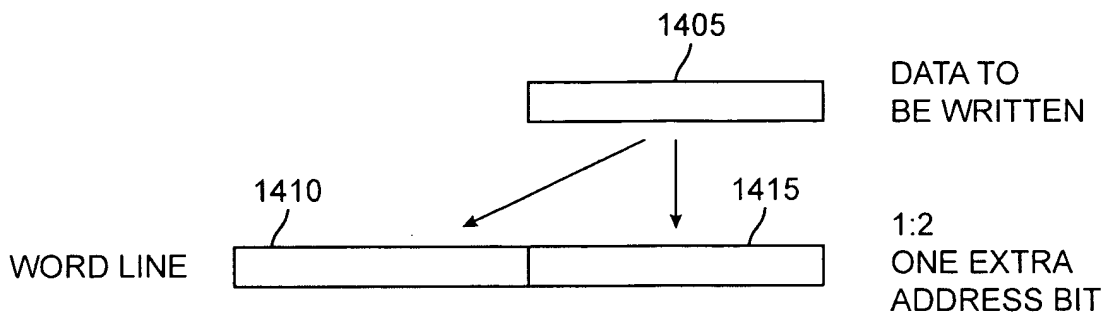


FIG. 14A

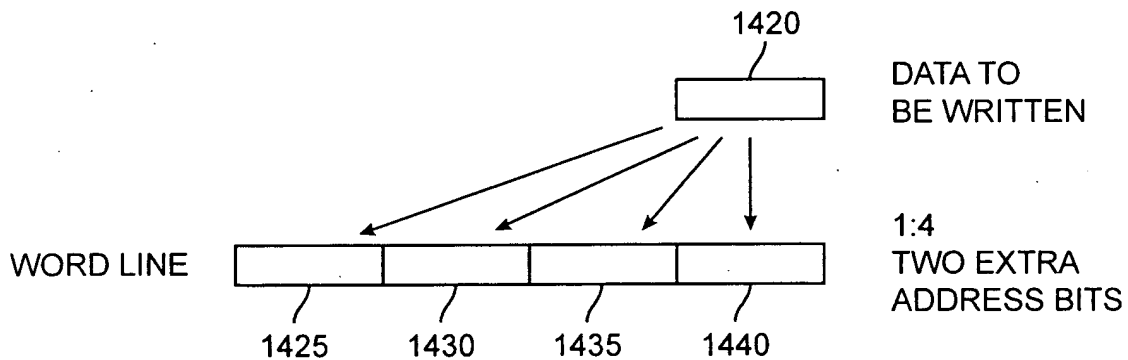


FIG. 14B

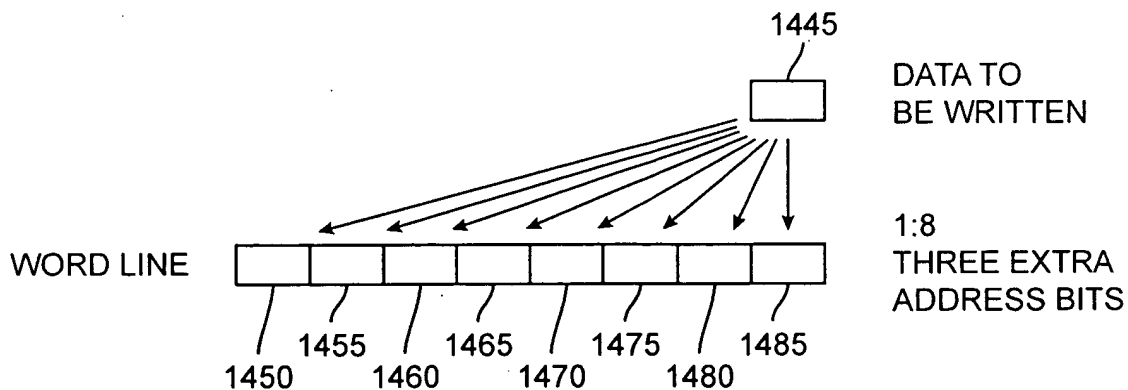
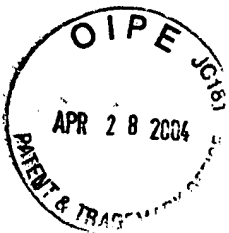


FIG. 14C



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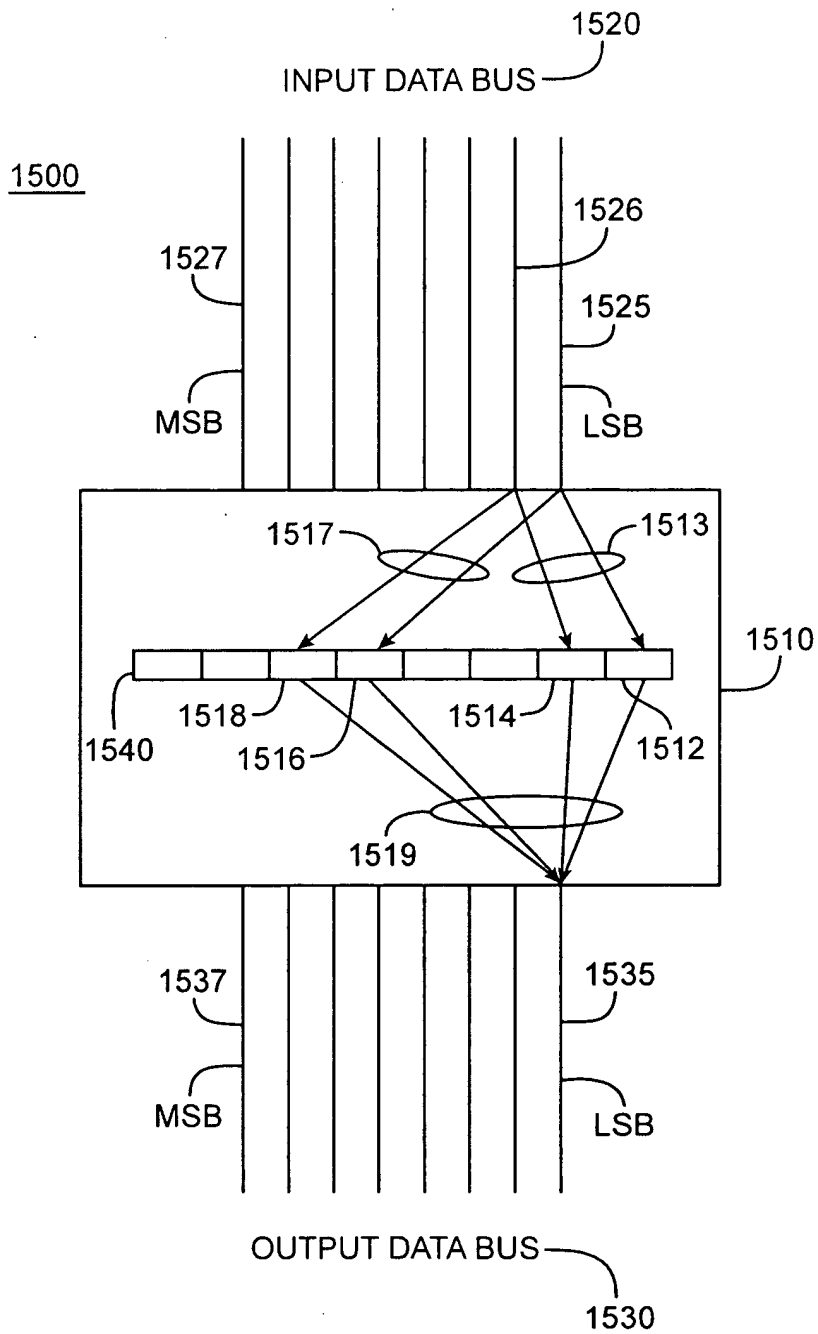
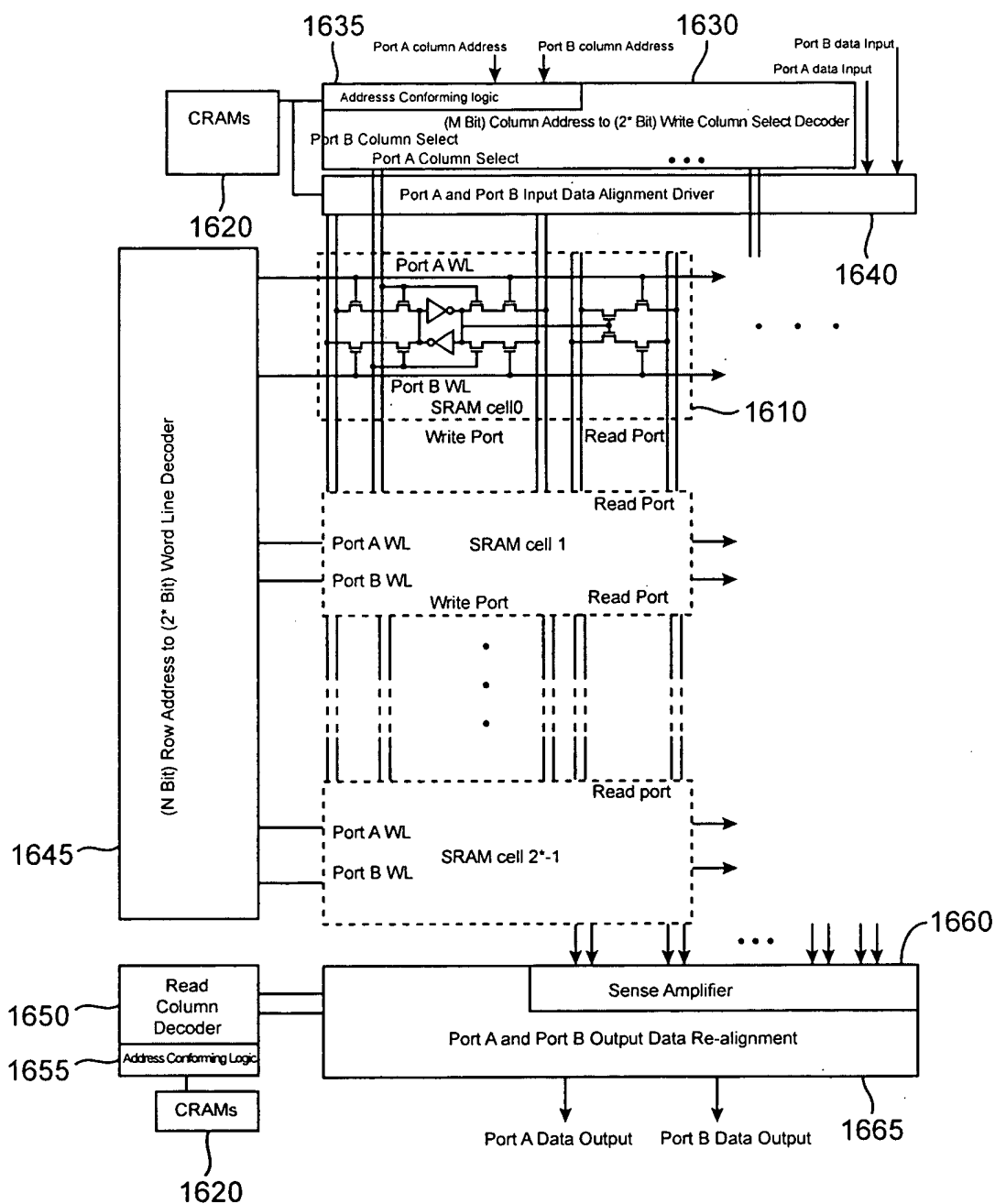


FIG. 15

1600



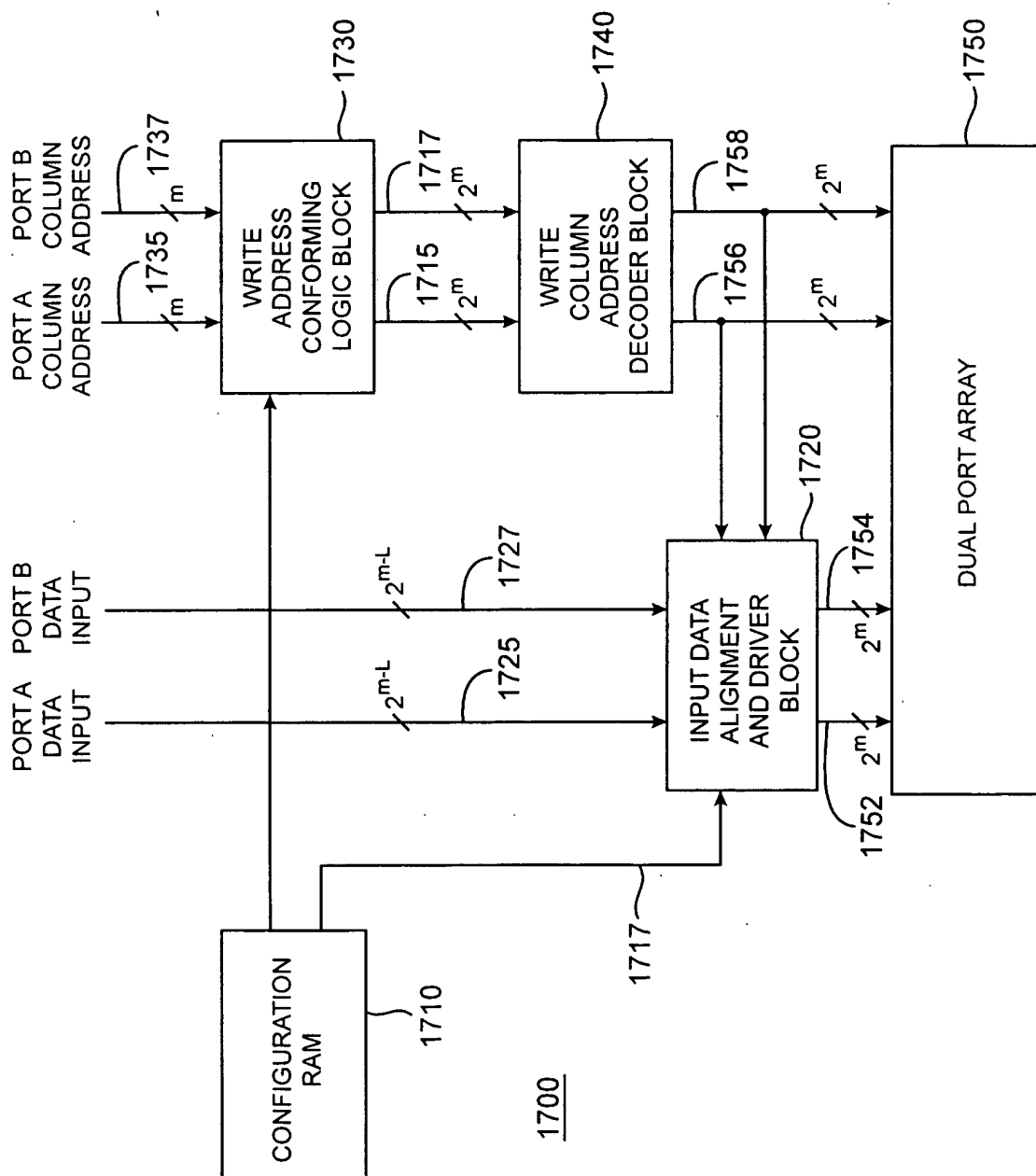
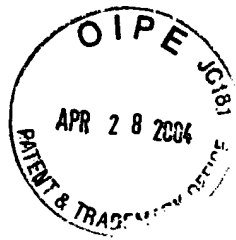


FIG. 17

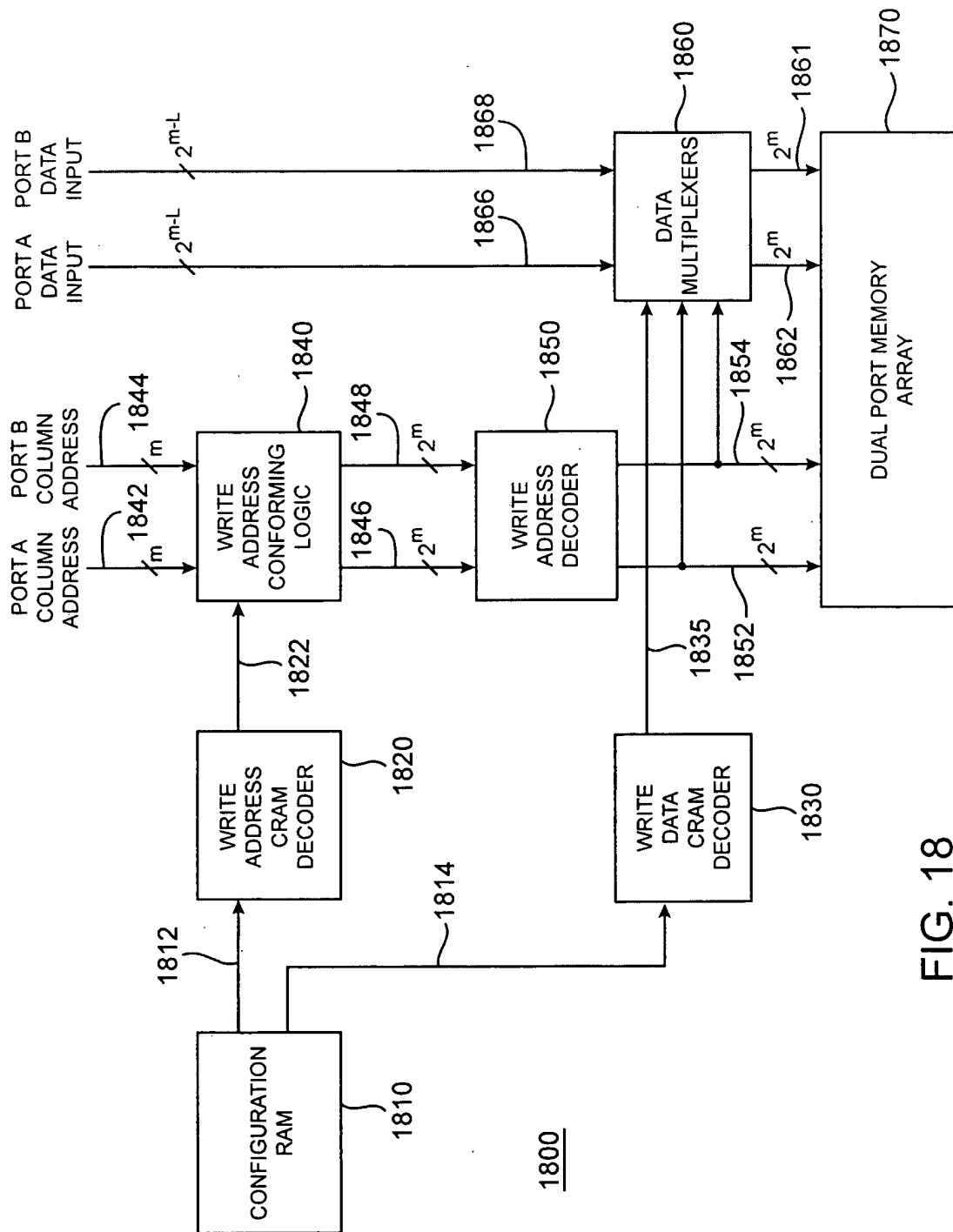


FIG. 18



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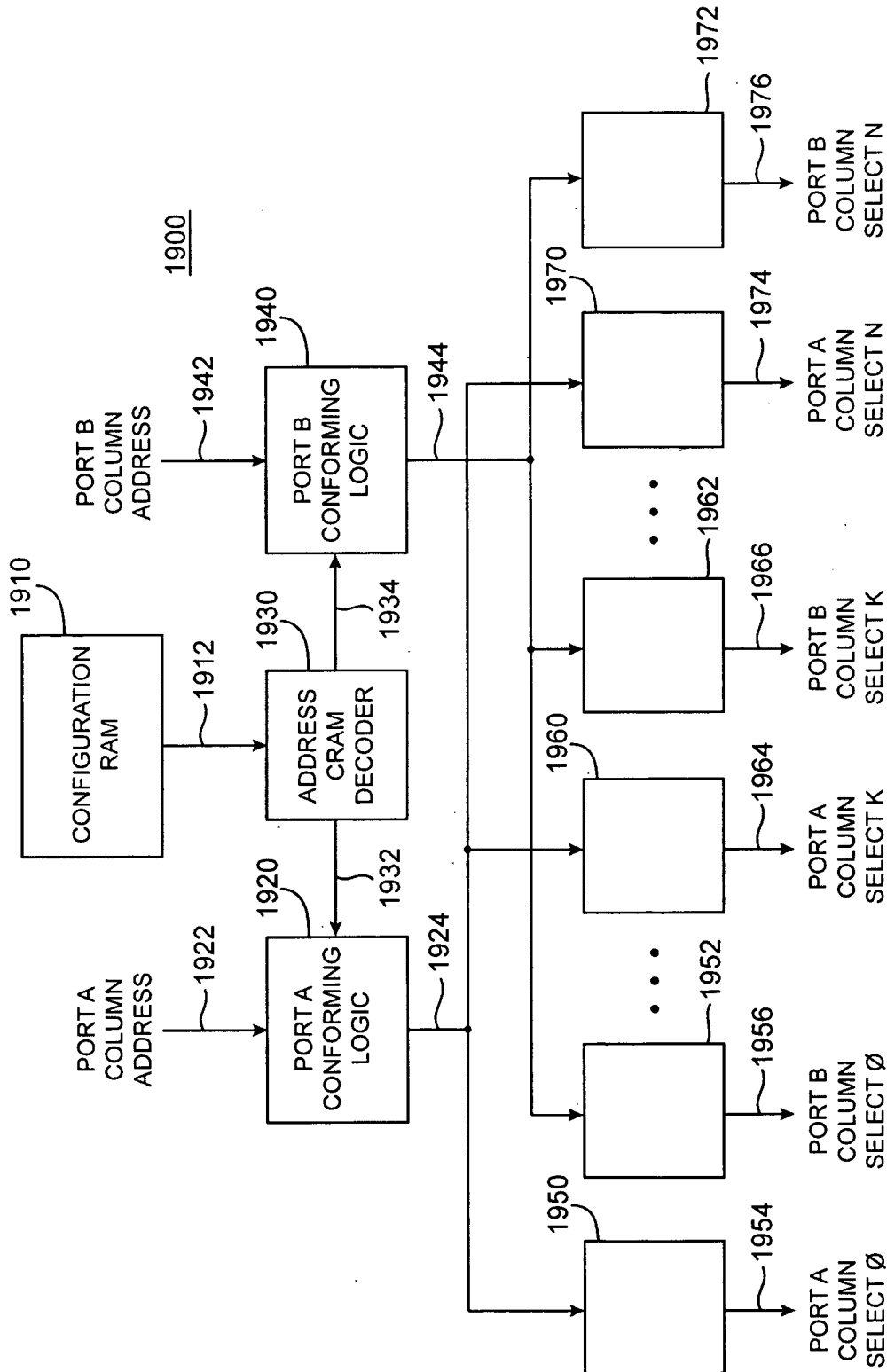
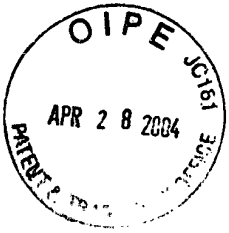


FIG. 19



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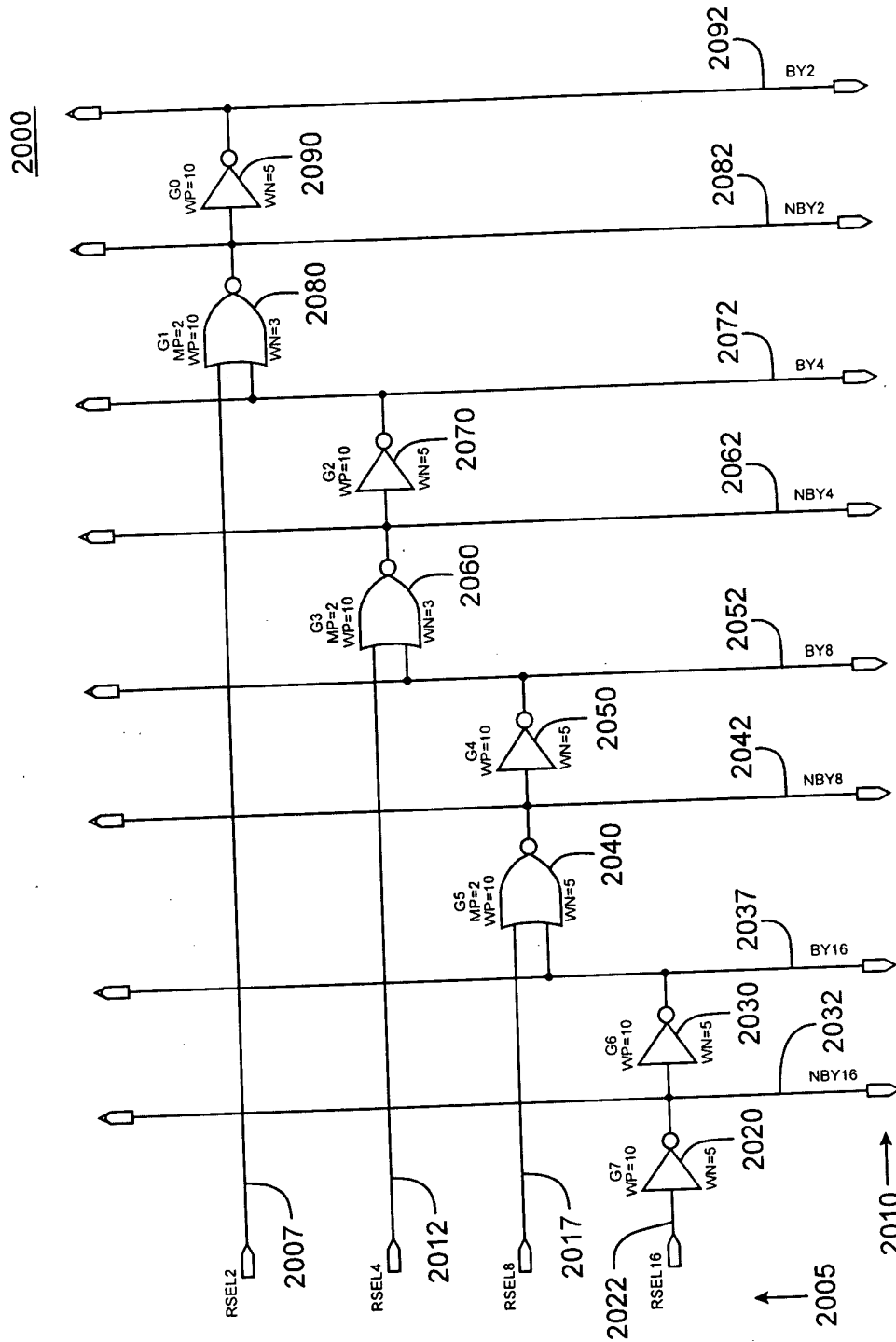


FIG. 20



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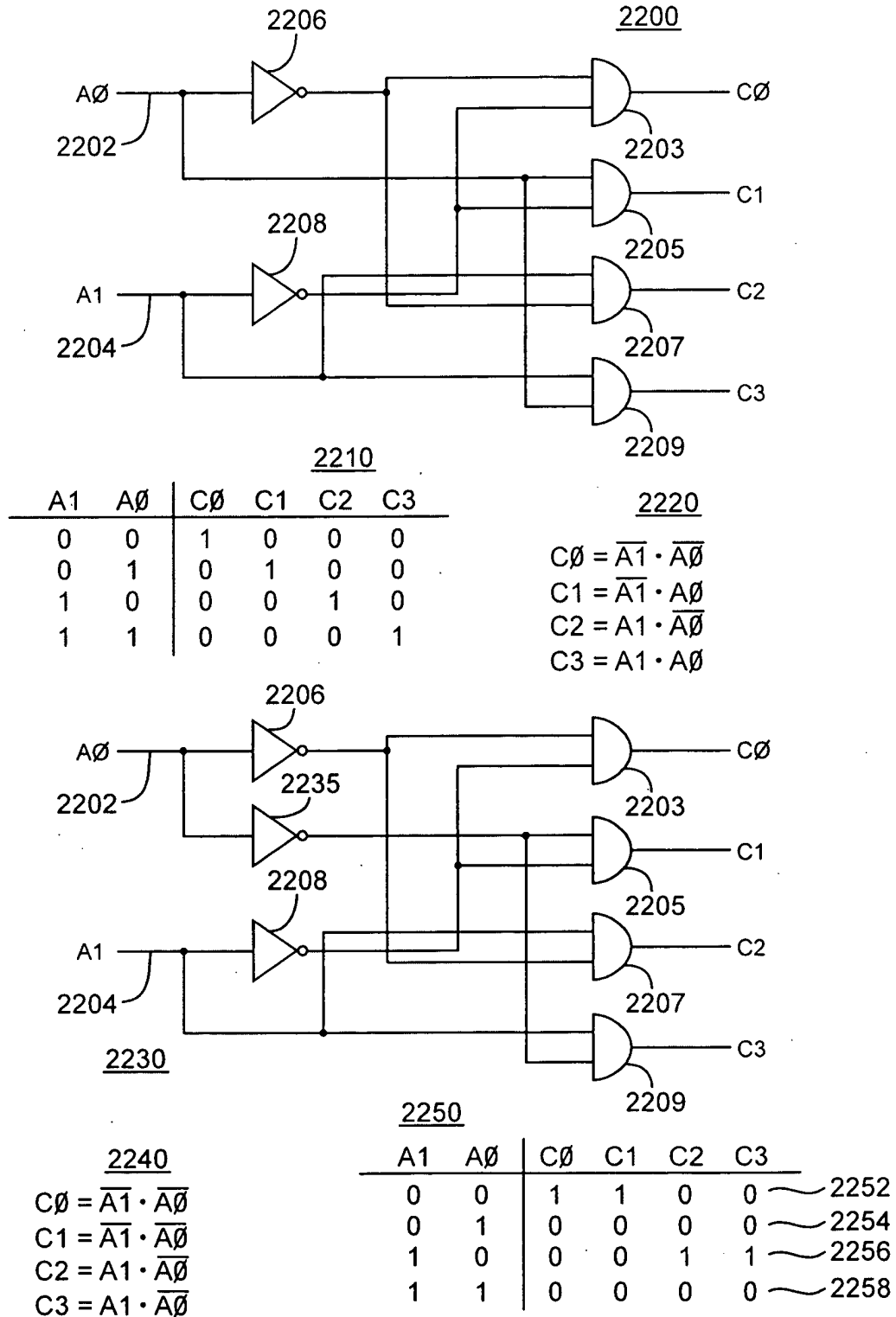
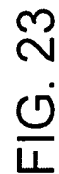
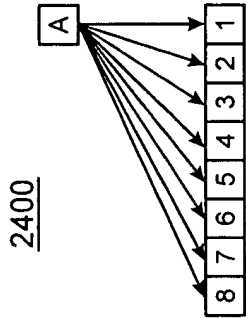
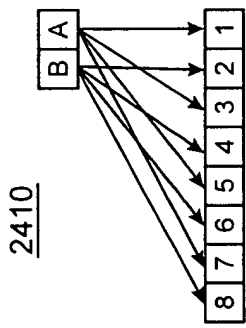
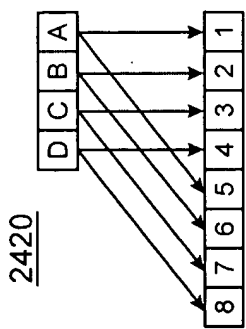


FIG. 22





2430

MUX	# bits / word				INPUTS	TYPE MUX
1	A	A	A	A	A	1:1
2	A	A	B	B	A, B	2:1
3	A	A	A	C	A, C	2:1
4	A	A	B	D	A, B, D	3:1
5	A	A	A	A	A	1:1
6	A	A	B	B	A, B	2:1
7	A	A	A	C	A, C	2:1
8	A	A	B	D	A, B, D	3:1

2440

MUX	OUTPUTS	TYPE MUX
A	8, 7, 6, 5, 4, 3, 2, 1	1:8
B	8, 6, 4, 2	1:4
C	7, 3	1:2
D	8	1:1

FIG. 24



FIG. 25



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2600

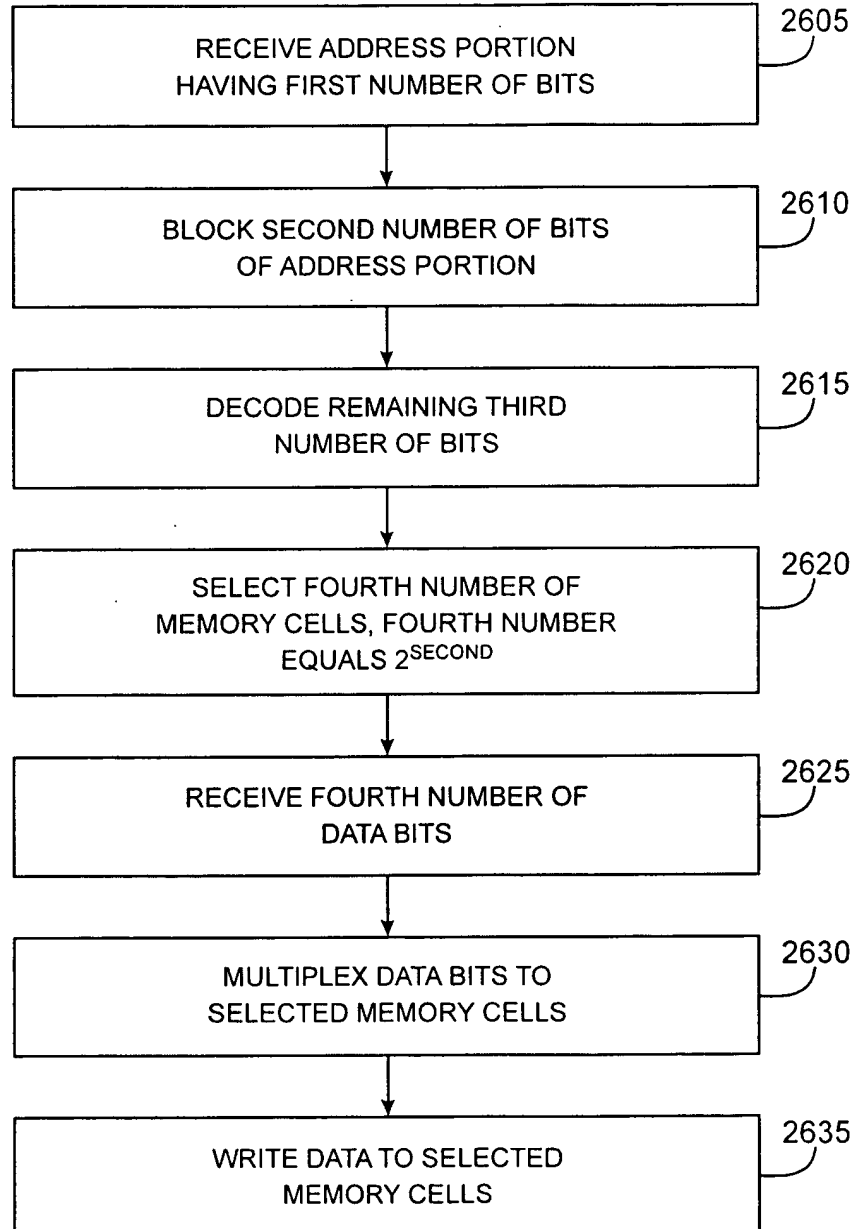
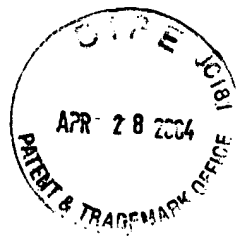
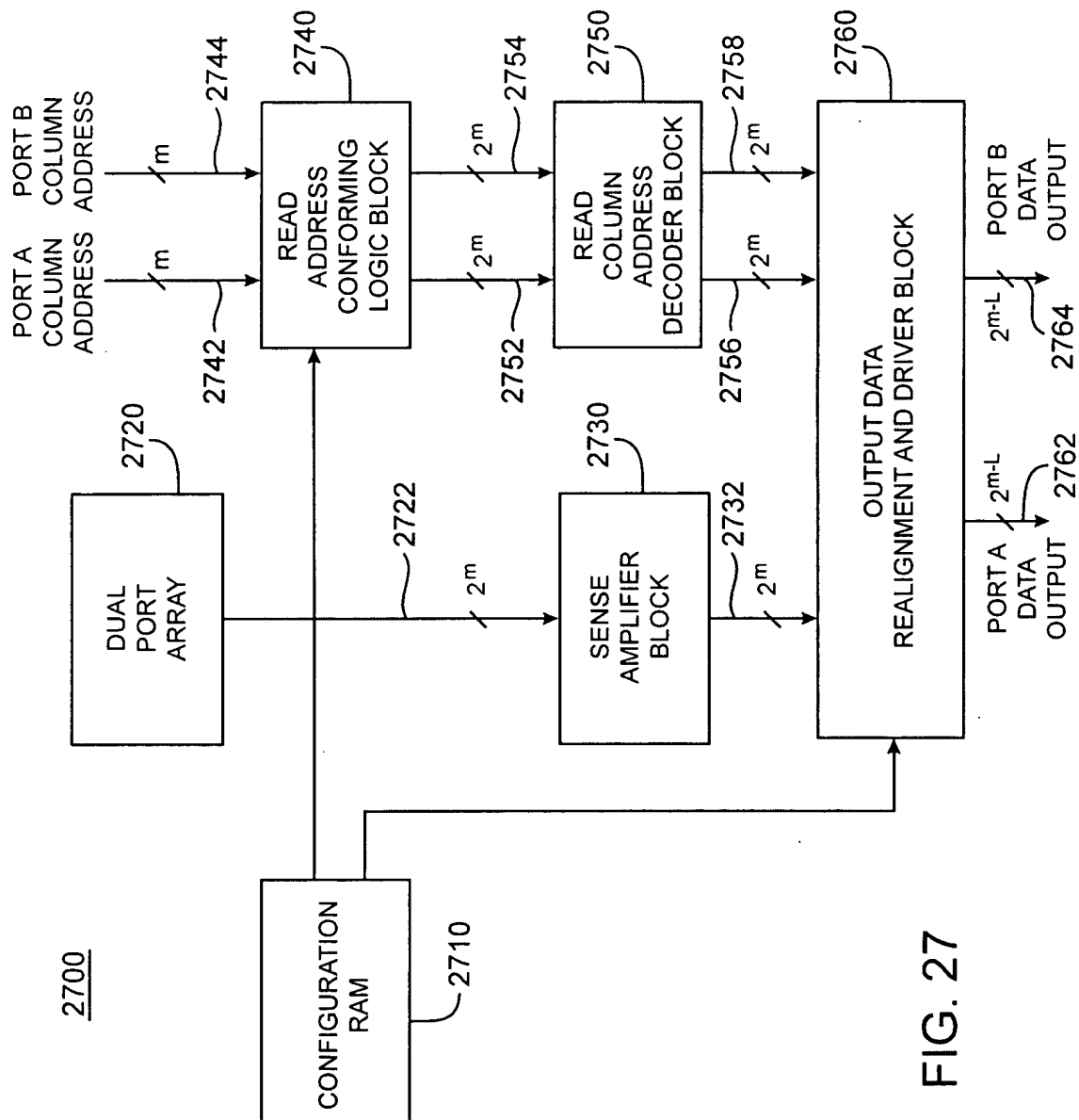


FIG. 26



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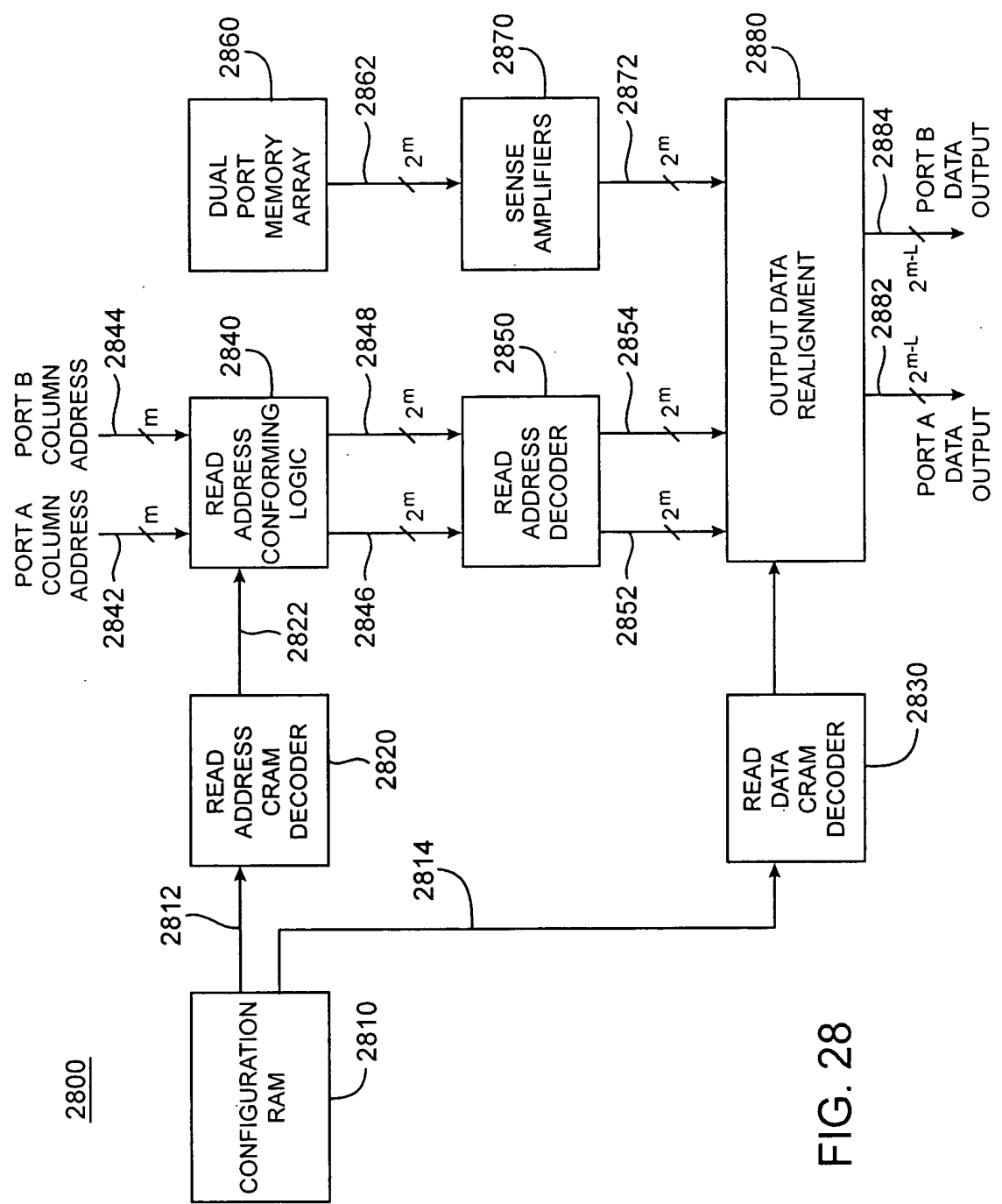
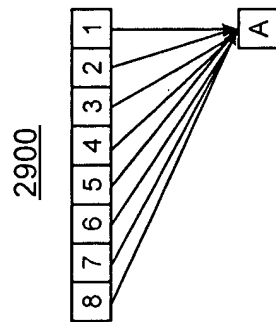
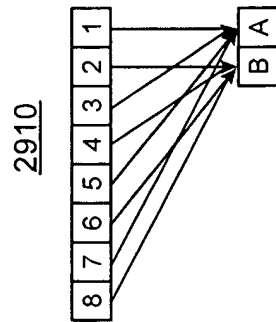
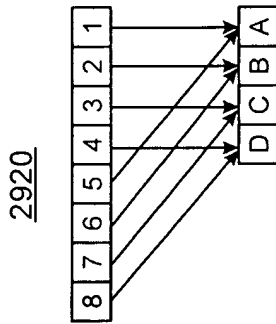
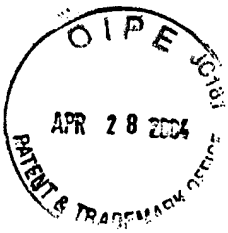


FIG. 28



2930

MUX	# bits / word				INPUTS	TYPE MUX
	1	2	4			
1	A	A	A		A	1:1
2	A	B	B		A,B	1:2
3	A	A	C		A,C	1:2
4	A	B	D		A,B,D	1:3
5	A	A	A		A	1:1
6	A	B	B		A,B	1:2
7	A	A	C		A,C	1:2
8	A	B	D		A,B,D	1:3

2940

MUX	INPUTS				TYPE MUX
	1	2	4	8	
A	8,7,6,5,4,3,2,1				8:1
B	8,6,4,2				4:1
C	7,3				2:1
D	8				1:1

FIG. 29

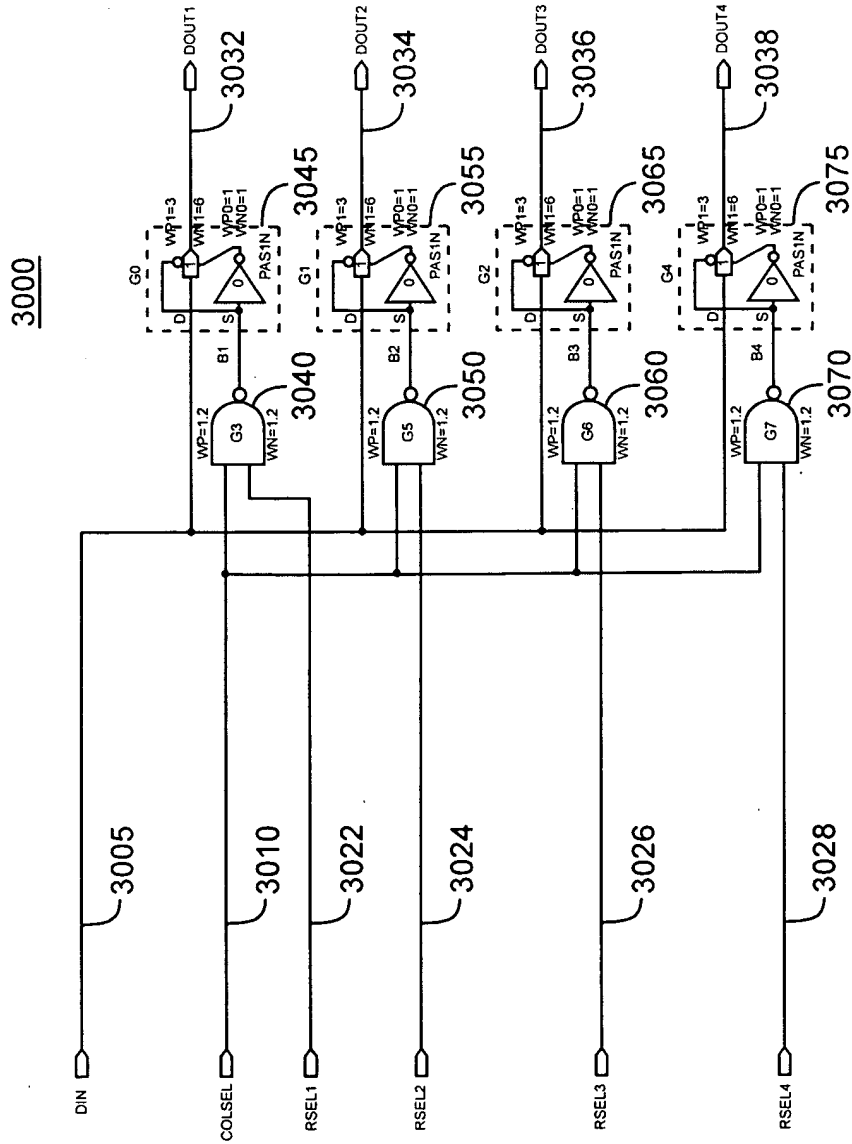


FIG. 30



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3100

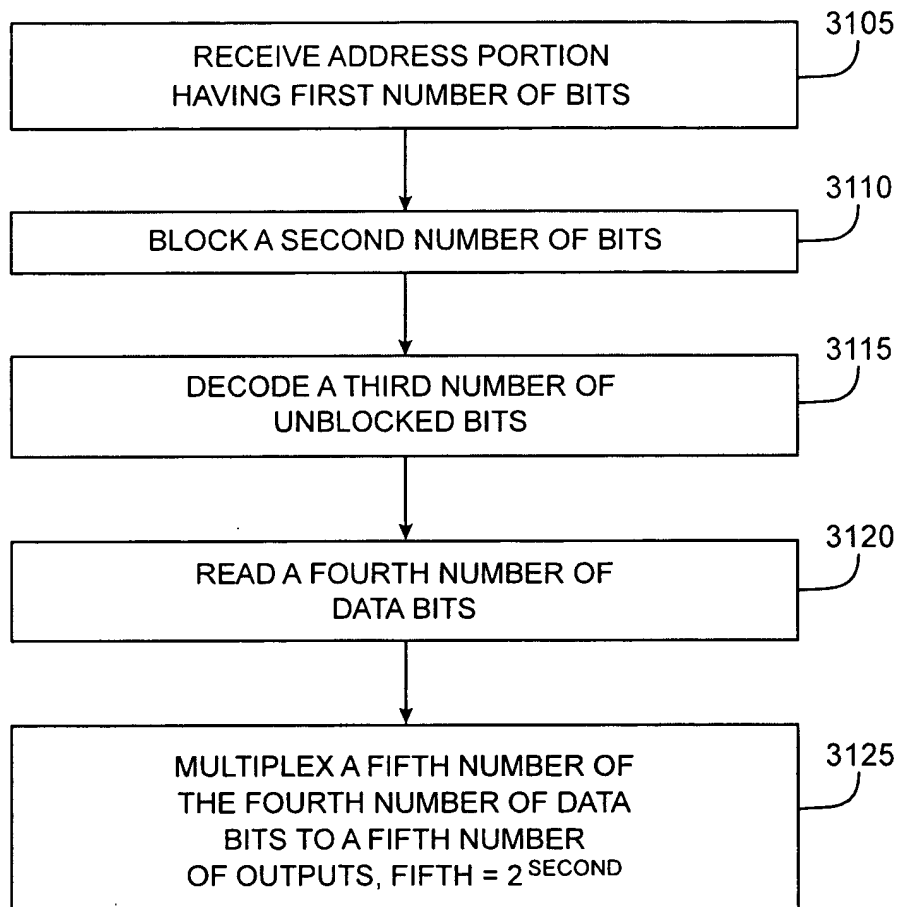


FIG. 31